

Japanese Unexamined Patent Publication**No. 2002-156950 (Tokukai 2002-156950)**

The following is a partial English translation of exemplary portions of non-English language information that may be relevant to the issue of patentability of the claims of the present application.

Claims:**1. A liquid crystal display comprising:**

a liquid crystal panel; and

a light source illuminating the liquid crystal panel,

wherein

the light source has a cycle which is made up of a period in which the light source exhibits a first radiation luminance and a period in which the light source exhibits a second radiation luminance; and

the light source has a control circuit which changes the time ratio of the first radiation luminance and the second radiation luminance in the cycle on the basis of externally supplied display data

2. The liquid crystal display of claim 1, wherein:

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the period in which the light source exhibits the first radiation luminance is longer than the period in which the light source exhibits the second radiation luminance; and

the control circuit sets the time ratio of the first radiation luminance in the cycle to less than 50% when the display data represents a moving image and 50% or more when the display data represents a still image.

3. The liquid crystal display of claim 1, wherein the second radiation luminance is in effect 0.

4. The liquid crystal display of claim 1, wherein the control circuit includes:

a data storage storing at least a frame of display data;

a data compare section comparing associated pixels between the display data stored in the data storage and input display data; and

a pulse control section supplying a signal controlling the time ratio of the first radiation luminance in the cycle in accordance with the comparison by the data compare section

5. The liquid crystal display of claim 4, wherein:

the data stores data for some of the pixels in the

display panel as comparison pixels; and

the data compare section compares the data for the pixels stored as the comparison pixels and pixels data of corresponding input data.

[0068] ... In addition, even if the screen has a high net luminance, sufficient luminance is secured by setting the luminance in the stop period to a certain specified luminance, instead of absolutely 0 as shown in Figure 12. It is of course desirable if the period in which the luminance in the stop period is raised is somewhat shortened.

Figure 12

- A Sync signal
- B One cycle
- C Light source luminance waveform, duty 50%, high luminance
- D Light source luminance waveform, duty 50%, medium luminance
- E Light source luminance waveform, duty 50%, low luminance
- F Luminance; dark level